

## OPTIMAL DESIGN OF A SINGLE-INPUT PARALLEL DC-DC CONVERTER DESIGNED BY SWITCHED CAPACITOR TECHNIQUES

KEI EGUCHI<sup>1</sup>, SAWAI PONGSWATD<sup>2</sup>, KITTI TIRASESTH<sup>2</sup>  
HIROFUMI SASAKI<sup>3</sup> AND TAKAHIRO INOUE<sup>4</sup>

<sup>1</sup>Department of Technology Education  
Shizuoka University

836, Ohya, Suruga-ku, Shizuoka, 422-8529, Japan  
ekeguch@ipc.shizuoka.ac.jp

<sup>2</sup>Faculty of Engineering  
King Mongkut's Institute of Technology Ladkrabang  
Ladkrabang, Bangkok, 10525 Thailand  
{ klsawai; ktkitti }@kmitl.ac.th

<sup>3</sup>School of Industrial Engineering  
Tokai University  
9-1-1 Toroku, Kumamoto-shi, Kumamoto, 862-8652 Japan  
hsasaki@ktmail.tokai-u.jp

<sup>4</sup>Department of Electrical and Computer Engineering  
Kumamoto University  
2-40-1, Kurokami, Kumamoto, 860-8555, Japan  
pt-inoue@cs.kumamoto-u.ac.jp

Received October 2008; revised March 2009

**ABSTRACT.** *In the design of parallel-connected power converters, theoretical analysis to clarify the influence of process fluctuation is important, because the characteristic of a converter which consists of many circuit components is strongly influenced by the fluctuation of those components. Concerning parallel-connected switched-capacitor (SC) DC-DC converters, 1. the optimal setting of a duty factor to achieve best efficiency and 2. the change in characteristics caused by fluctuation of circuit components are analyzed in this paper. The validity of the theoretical analyses is confirmed by SPICE simulations and experiments. The results of this study are as follows: 1. The optimal setting of duty factor  $D$  can be derived by the proposed theoretical formulas, because the theoretical results correspond well with the simulated results. 2. The fluctuation of on-resistances affects power efficiency in proportion to the decrease of output load  $R_L$ . The value of the total SC resistance is important in power efficiency  $\eta$ , for example, an increase of 20 % in the total SC resistance causes a decrease of 2.2 % in efficiency when  $R_L = 5\Omega$ . 3. When fluctuation of input voltages is caused, the influence on power efficiency  $\eta$  increases according to output load  $R_L$ . 4. In the case of the fluctuation of capacitance, the influence of ripple factor  $R$  depends on fluctuating directions of capacitances.*

**Keywords:** DC-DC converters, Power converters, Switched capacitor circuits, Fluctuation analysis, Parallel-connected converters, Power efficiency

1. **Introduction.** In the field of portable electronics, switching converters [1-24] which offer large current outputs are becoming important due to the voltage lowering of semiconductor processes. For this reason, design analyses have been performed concerning parallel-connected DC-DC converters employing buck converters or boost converters [1]-[11]. For example, concerning single-input parallel DC-DC converters, parallel-connected